

1857859
Attorney's Docket 7040-30

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Heinemann, et al.

Examiner: Unknown

Ser. No.: 09/857,857

Art Group: Unknown

Title: BIPOLAR TRANSISTOR AND METHOD FOR PRODUCING SAME

Filed: 11 June 2001

Date: 23 August 2001

PRELIMINARY AMENDMENT

This Preliminary Amendment is filed with the completion of the filing requirements in this case, which is a national stage entry of PCT case DE99/03961, which in turn claims priority from an application filed in Germany as 198 57 640.4 on 14 December 1998. The fees for the claims should be calculated based on the claims remaining after the entry of this Preliminary Amendment, which results in 14 total and 4 independent claims. Consistent with the modifications to 37 CFR §1.121, the applicant has provided the amended claims in a clean form on the attached sheets.

Amendments to the Disclosure

The specification as filed has been altered from the literal translation document received to delete information above the title, to insert headings according to US practice, and to insert paragraph numbering in lieu of line numbering, but no amendments are made to the content. That copy of the specification should be used for examination.

Amendments to the Figures

None at this time.

Amendments to the Abstract:

None at this time

Amendments to the Claims

Please amend the claims as follows:

1. (amended) A procedure for the manufacture of a bi-polar transistor, during which structured regions consisting of a collector region [(112)] and an insulation region that [(113) - which] surrounds the collector region [(112) -] are produced on a monocrystal substrate layer [(111)], a base layer [(115)] and, by means of epitaxy, a cap layer [(116)] are produced over the